



DCIS2010

XXV Conference on Design of
Circuits and Integrated Systems

November 17-19
Gran Hotel Meliá Salinas, Lanzarote
Canary Islands, Spain

25
DCIS
1981-2010



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Welcome to DCIS 2010!

The purpose of this intense three-days conference is to bring together faculty, students and industry researchers to discuss recent developments and future directions in the field of micro and nano electronic circuits and integrated systems.

For this special 25th edition, DCIS moves to the idyllic island of Lanzarote, Spain, where its charm landscapes, surrounded by volcanic scenarios, will facilitate the transfer of innovative ideas and the creation of new ones.

DCIS 2010 is seeking papers that deal with the number of topics described below, although not limited to:

- Digital IC Design & Test
- Programmable Devices and Systems
- Logic and Architectural Synthesis
- System Design Methodologies
- Hw-Sw Codesign
- Embedded Design & SoC
- Digital Signal Processing
- Analog-Digital Interface Design
- Analog & Mixed-Signal IC Design & Test
- RF IC Design & Test
- Smart Objects & Wireless Applications
- Communication Systems
- Power Electronics - Devices & Systems
- Low Power Design
- Integrated Sensors & Actuators MEMS
- Nanoelectronics
- Modeling, Simulation and Synthesis Techniques
- Testability and Test Techniques
- Failure Analysis and Reliability
- Industrial Applications
- Bioinspired Systems
- Reconfigurable Computing
- Defect and Fault Tolerance Techniques

PAPER SUBMISSION English is the official language of the Conference. Full papers must be prepared following the rules encountered at the DCIS official webpage (www.dcis.org). Selected manuscripts from the conference will be included in a special issue of the Elsevier Embedded Hardware Design (MICPRO) journal, scheduled to appear on the first half of 2012.

IMPORTANT DATES

Submission deadline: March 29, 2010
Notification of acceptance: July 2, 2010
Camera-ready version: July 26, 2010